

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	
Filing Date	
Inventor	Luan C. Tran
Assignee	Micron Technology, Inc.
Group Art Unit	
Examiner	L. Schillinger
Attorney's Docket No	MI22-1637
Title: Semiconductor Processing Methods Of Formir	
Processing Methods Of Forming Dynamic Random Access Memory Circuitry,	
And Related Integrated Circuitry	•

RESPONSE TO JUNE 2, 2004 OFFICE ACTION

Ey372467966

To:

From:

Mail Stop Amendments

Assistant Commissioner for Patents

P.O. Box 1450

Alexandria VA 22313-1450

James D. Shaurette

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Sir:

Please enter the following amendments prior to examining the above identified application:

<u>AMENDMENTS</u>